



PTO/SB/08A (04-03)

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of

1

Complete If Known

Application Number

09/548,414

Filing Date

April 13, 2000

First Named Inventor

Takahiro OGUCHI

Art Unit

2823

Examiner Name

Joannie GARCIA

Attorney Docket Number

36856.743

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

Joanne A. Barker

Date
Considered

January 26, 2005

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STATEMENT BY APPLICANT**

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Examiner Name

J. GARCIA

Attorney Docket Number

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title Of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JAG	4	ZHIXIONG et al., "LATERALLY CAPACITY SENSED ACCELEROMETER FABRICATED WITH THE ANODIC BONDING AND THE HIGH ASPECT RATIO ETCHING", Solid-State and Integrated Circuit Technology 1998 Proceedings, 5 th International Conference in Beijing, China, Oct. 21-23, 1998, pp. 921-924, IEEE, Piscataway, NJ.	
JAG	5	MOCHIDA et al., "A MICROMACHINED VIBRATING RATE GYROSCOPE WITH INDEPENDENT BEAMS FOR THE DRIVE AND DETECTION MODES", Micro Electro Mechanical Systems 1999, MEMS '99, 12 th IEEE International Conference in Orlando, FL, Jan. 17-21, 1999, pp. 618-623, IEEE, Piscataway, NJ.	
JAG	6	KLASSEN et al., "SILICON FUSION BONDING AND DEEP REACTIVE ION ETCHING: A NEW TECHNOLOGY FOR MICROSTRUCTURES", Proc. Of Transducers '95, 8 th International Conference on Solid-State Sensors and Actuators, Stockholm, Sweden, June 25-29, 1995, pp. 556-559, Vol. 1.	
JAG	7	FRANSSILA et al., "ETCHING THROUGH SILICON WAFER IN INDUCTIVELY COUPLED PLASMA", Microsystem Technologies, , April 2000 (2000-04), pp. 141-144, vol. 6, no. 4, Berlin, DE.	

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